

<b>FORM PTO-1449</b> U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO.	SERIAL NO.
	520.36259CX1	Not yet assigned
	APPLICANT ENDO, et al	
	FILING DATE June 14, 2001	GROUP 266T 266

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SH	5 4 5 2 2 9 6	9/95	Shimizu	370	60.1	
	5 4 6 7 3 4 9	11/95	Huey et al	370	60.1	
	5 4 6 3 6 2 1	10/95	Suzuki	370	399	
	5 8 0 9 0 2 5	9/98	Timba	370	397	
	5 4 2 0 8 5 8	5/95	Marshall et al	370	395	
SH	5 9 4 6 3 1 3	8/99	Allan et al	370	397	

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
						YES	NO
SH	8 1 2 5 6 9 2	5/96	Japan	H04L	12/66	XX	

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

SH	P. Newman, et al, "Flow Labeled IP: A Connectionless Approach to ATM" Proc. IEEE Infocom, M. 1996, pp. 1-10.
	C. Hou, "Routing Virtual Circuits with Timing Requirements in Virtual Path Based ATM Networks, Department of Electrical and Computer Engineering, The University of Wisconsin, 3/24/96, pp. 320-328.
	P. Newman, et al, "IP Switching and Gigabit Routers", IEEE Communications Magazine, January 1997, pp. 64-69.
	H. Schneider, "The Concept of Virtual Paths and Virtual Channels in ATM-Networks", X000315092, March 3, 1990, pp. 63-72.
	H. Esaki, et al, "High Speed Datagram Delivery over Internet Using ATM Technology" IEICE Trans. Commun. Vol. E78-B, No. 8, August 1995, pp. 1208-1217.
SH	G. Chandranmenon et al, "Trading Packet Headers for Packet Processing", IEEE/ACM, Transactions on Networking, vol. 4, no. 2, April 1996, pp. 141-152.

EXAMINER

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DATE CONSIDERED

12.23.04

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered.  
 Include copy of this form with next communication to applicant.